

Amendments to the Specification:

Please replace the TITLE beginning at page 1, line 1 with the following amended TITLE:  
**NODE ID DISCOVERY IN A MULTI-NODE SYSTEM**

Please replace the paragraph beginning at page 2, line 4 with the following amended paragraph:

A scalable node controller 52 connects one or more microprocessors 58, system RAM 60, and system ROM 62 to switch 56. An I/O hub controller 54 connects input/output devices, such as peripheral component interconnect (PCI) bridge 64, small computer system interface (SCSI) bridge 66 and hard disks 68 to switch 56. Additionally, system ROM 62 may also be connected to I/O hub controller 54 in addition to (or in substitution for) any ROM connected to scalable node controller 52. Bridges 64 and 66 allow for the interfacing of the computer system 50 (which incorporates switch topology) and a standard communication bus 69, thus allowing a switch-based system to use a standard communication bus to communicate with many card-based peripherals (e.g., video cards, network adapters, modems). As stated above, switch 56 can be, for example, a cross bar, a switch, or a router, or other implementations. ~~switch, a router, or a~~

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